

Connor Masterson cmasterson2000@gmail.com +1(310)795-6351
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Education University of California, Santa Cruz, Jack Baskin School of Engineering 2018 - Present

- Will be a Junior in Fall 2020, pursuing Computer Engineering BS Degree
- GPA as of 12-24-2020: 3.68/4.00, Received Dean's Honors last five quarters

Skills

- Programming: C, C++, Python, Verilog, Golang, Bash, RISC-V, and MATLAB
- Computer Architecture and Logic Design
- Working in Linux/Unix, virtual, remote environments
- Working with OpenMP, Numpy, PyTorch, Vivado, [ESESC](#), MPLABX, SolidWorks, Microsoft Office
- Leadership and Team Building

Experience

- Peer Tutor for CSE30 - Abstractions in Python *September 2020 - Present*
Tutored students in class that covered OOP, Machine Learning, SAT Solving, Graphs, Dynamic Programming, Scheduling, Motion Detection
- Undergraduate Research Assistant *June 2020 - Present*
Worked on and contributed to the Graph Algorithm Platform (GAP) Benchmark Suite with Professor [Scott Beamer](#) and developed ways for the program to build graphs in place (reduce peak memory usage). Helped convert the codebase from Jacobi to Gauss-Seidel style Pagerank. More information about this project can be found at: gap.cs.berkeley.edu and github.com/sbeamer/gapbs

Relevant Coursework

- Computer Architecture (both undergraduate and graduate)
 - Memory Hierarchies, RISC-V, Branch Prediction, Caches, Coherence & Consistency, Virtual Memory, Pages, DRAM, SRAM, Dynamic Scheduling, Out-of-order Execution, VLIW Coherence, Multithreading, Vectors
- Digital Logic Design
 - Logic Design, Verilog, Static Timing Analysis, CMOS/PMOS/NMOS transistors, State Machines
- Electrical Circuit Analysis and Design
 - KCL, KVL, Analysis Methods, Op-Amps, Power Sources
- Advanced Programming
 - Polymorphism, Templates, Sockets & Ports
- Abstract Data Structures and Algorithms
 - Abstract Data Types, Computational Complexity, Algorithm Analysis
- Embedded Systems and C Programming
 - Programming in an Embedded Environment, State Machines, Event-driven Programming
- Physics: Fluid Mechanics, Waves, and Optics; Electricity and Magnetism
- Math: Ordinary and Partial Differential Equations; Linear Algebra; Probability and Statistics; Multivariable Calculus; Discrete Mathematics
- To Be Taken by Summer of 2021: Logic Design in Verilog, Computer Systems

Projects

- Member of the Vertical Architectures, Memory, and Algorithms research group within the Hardware Systems Collective (name for all computer engineering research) at UC Santa Cruz.
VAMA: <https://github.com/ucsc-vama> HSC: <https://hsc.ucsc.edu/>
 - SlugSat: Treasurer of UC Santa Cruz's SlugSat club which is a team of electrical engineers, computer engineers, physics students working to build a CubeSat. More info about the project can be found at the website here: <https://slugsat.soe.ucsc.edu/home-page> SlugSat is also a part of/helped create the Blue Dot Consortium. It is a joint effort between CubeSat teams at universities such as Berkeley, Stanford, UCLA, Harvard, Rice, Cal Poly Pomona, and more.
- Other Interests (in no particular order):** Cooking, Linux, Distributed Systems, FOSS, Beach Volleyball, RISC-V